

TRANSMITTAL OF FORMAL DRAWINGSDocket No.
BUR919990299US2

In Re Application Of: Adkisson et al.

Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/064,171	06/18/2002	D. Owens	30743	2811	8362

Invention: **DOUBLE GATE TRENCH TRANSISTOR**

Address to:
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Transmitted herewith are:

7 sheets of formal drawing(s) for this application.

☒ Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).
Signature

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Dated: May 24, 2006

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on

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